

**Subject: Invitation to Faculty Workshop – Empowering Electronics Educators**

Dear Faculty Members,

The Department of Electronics, Annasaheb Magar Mahavidyalaya, Hadapsar, in association with Savitribai Phule Pune University, is pleased to invite you to participate in a One-day Workshop on:  
Empowering Electronics Educators: Practical Training and In-depth Syllabus Orientation for Second Year Teaching

**Date:- 13 August 2025**

**Day:- Wednesday**

**Time:- 10:00 am**

**Venue:- Commerce Hall, Annasaheb Magar Mahavidyalaya, Hadapsar, Pune-28.**

This workshop is part of our ongoing efforts to enhance the understanding of the syllabus and overcome challenges in conducting practical sessions with confidence and ease.

Workshop Highlights:

- Detailed orientation of the second-year Electronics syllabus
- Practical execution strategies for effective teaching
- Overcoming classroom and laboratory challenges
- Fostering confidence for stress-free teaching

We strongly believe this session will be highly beneficial for faculty members and will provide valuable insights for improving both teaching methodology and student engagement.

Registration Details:

To confirm your participation and indicate your interest in presenting during the workshop, please complete the Google Form below by 12th August 2025:

Google Form Link

<https://forms.gle/fSxCu1Q6BREVW8Vn9>

We sincerely look forward to your active participation and valuable contributions.

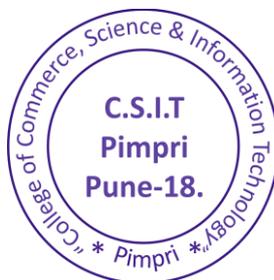
Best regards,

Dr. Vilas More

Coordinator & Head,

Department of Electronics

Annasaheb Magar Mahavidyalaya, Hadapsar, Pune





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College of Commerce, Science &  
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S. No. 29/1+2A, Pune-Mumbai Highway,  
Pimpri, Pune - 411 018.



PDEA'S  
ANNASAHEB MAGAR MAHAVIDYALAYA,  
HADAPSAR, PUNE-28



**ONE DAY WORKSHOP  
ON  
EMPOWERING ELECTRONICS  
EDUCATORS: "PRACTICAL  
TRAINING & IN-DEPTH  
SYLLABUS ORIENTATION  
FOR SECOND YEAR  
TEACHING"**

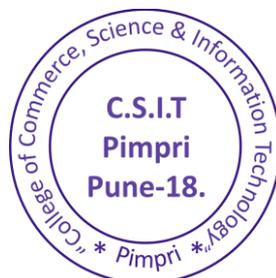
**Class:-  
S.Y.B.Sc.(CS) & S.Y.BCA**

**No Registration Fees**

**Date & Time  
13th August 2025  
(Wednesday)  
10:00am to 03:30pm  
Venue: Commerce  
Seminar Hall**

**Coordinator  
Dr. V.B. More**

**Principal  
Dr. N. L. Ghorpade**



*N.L. Ghorpade*

**Principal**

College of Commerce, Science &  
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Pimpri, Pune - 411 018.

## Report of Event

### “One Day Workshop on Empowering Electronics Educators: Practical Training & In-Depth Syllabus Orientation for Second Year Teaching”

Date	: 13/08/2025
Time	: 10.00am-3.30pm
Venue	: Annasaheb Magar Mahavidyalaya, Hadapsar
Collaboration (If any)	:
Coordinator	: Dr. Vikas More Annasaheb Magar Mahavidyalay, Hadapsar

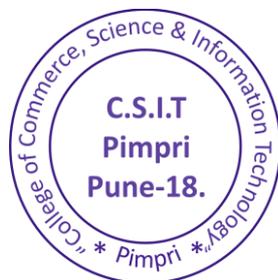
#### Introduction:

To bridge the gap between theoretical knowledge and real-world applications, **Annasaheb Magar Mahavidyalaya, Hadapsar** is organizing a **One-Day Workshop on “Empowering Electronics Educators: Practical Training & In-Depth Syllabus Orientation for Second Year BSc(CS) & BCA Teaching.”**

The workshop has been thoughtfully designed to provide hands-on exposure, strengthen conceptual understanding, and enable educators to effectively implement the revised electronic science syllabus.

The program is graced by eminent academicians and experts: **Dr. Neha Deshpande**, Coordinator, Board of Studies (Electronic Science), SPPU, (Abasaheb Garware College, Pune) and **Dr. Milind Bodke**, BOS Member, Department of Electronics (Modern College, Shivajinagar). Principal **Dr. Nitin Ghorpade** (Annasaheb Magar Mahavidyalaya, Hadapsar) as the **Chief Guest for the Inaugural Function**. **Dr. Pramod Patil**, Dean, Science & Technology, SPPU, Pune, as the **Chief Guest for the Valedictory Function**.

With this rich blend of academic expertise and practical insights, the workshop aims to empower educators with the tools, strategies, and confidence required for impactful teaching of second-year electronic science. The workshop aims to provide a comprehensive platform for faculty members to strengthen their understanding of the second-year electronics syllabus while simultaneously equipping them with practical training methodologies. By focusing on hands-on learning, innovative teaching practices, and syllabus mapping, this workshop will enable educators to deliver concepts more effectively in the classroom and laboratory. Through





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interactive sessions, demonstrations, and collaborative discussions, participants will gain insights into the latest teaching tools, lab practices, and industry-relevant applications.

### Objectives:

- 1 **To familiarize educators** with the second-year electronics syllabus and its practical orientation.
- 2 **To strengthen pedagogical skills** for delivering complex electronic concepts in a simplified and engaging manner.
- 3 **To provide hands-on training** with laboratory experiments, tools, and simulation software relevant to the syllabus.
- 4 **To bridge the gap** between theoretical concepts and their real-world/industry applications.
- 5 **To introduce innovative teaching strategies** that enhance student learning outcomes and active participation.

### Detailed Report:

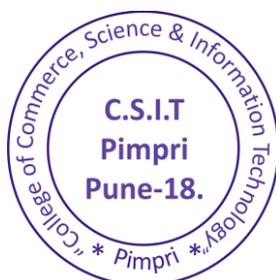
The program began with the **Inaugural Session**, graced by the presence of distinguished dignitaries. **Chief Guest: Dr. Neha Deshpande and Dr. Milind Bodke, Dr. Nitin Ghorpade, Dr. Prashant Mulay, Dr. Nikam L.K., Dr. Auti, Dr. More Vilas.** The Chief Guest, Dr. Neha Deshpande, in her inaugural address, emphasized the importance of blending practical training with syllabus orientation so that educators can better connect students to the fast-changing world of electronics. Dr. Milind Bodke highlighted the role of workshops in creating a collaborative learning environment for teachers. Their words set a positive and inspiring tone for the day. After the inaugural function, the Hands on training session started at Electronics Lab and BCA Lab :

#### **Session 1: Networking Simulations using Cisco Packet Tracer:**

Resource Person: **Dr. Deepak Kumbhar**, Head, BCA (Science), Modern College, Ganesh Khind, Pune. He introduced participants to Cisco Packet Tracer, a simulation software at Electronics Lab. The session covered designing, configuring, and analysing networks in a virtual environment

#### **Session 2: Embedded Systems with AVR Microcontroller & Microchip Tools**

Resource Person: **Dr. Muddasar Shaikh** Head, MGP Arts, Commerce & Science College, Ahilyanagar. The session was a real highlight. He walked the participants through the exciting world of embedded systems, starting from AVR microcontroller basics to hands-on

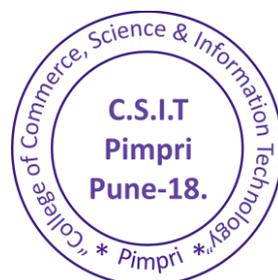


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programming examples. He also introduced Microchip Studio and development hardware, showing how educators could set up practical experiments with ease. The session made it clear how embedded systems are at the heart of modern electronics—from simple projects to real-world applications.

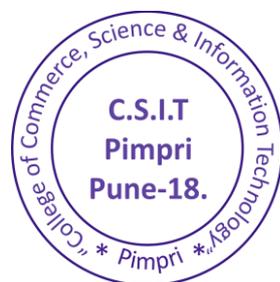
After the hands-on sessions, the program concluded with the Valedictory Function, graced by **Dr. Pramod Patil**, Dean, Science & Technology, SPPU, Pune. He appreciated the initiative of Annasaheb Magar Mahavidyalaya and encouraged teachers to bring innovation and experimentation into classrooms. The session ended with a heartfelt Vote of Thanks delivered by **Dr. Vilas More**, who expressed gratitude to all dignitaries, resource persons, participants, and organizing members for making the event a success. Many faculties from other colleges attended this workshop, and on behalf of ASM CSIT, **Asst.Prof. Priya Janjalkar** and **Asst.Prof. Chaitanya Hanote** participated actively in the workshop.



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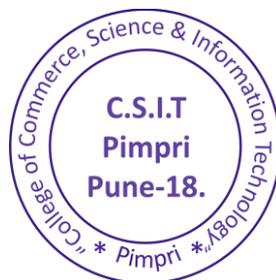


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**Outcomes:**

- 1 Enhanced Syllabus Understanding – Participants will gain a clear and structured understanding of the second-year electronics curriculum with practical orientation.
- 2 Improved Practical Competence – Faculty will develop confidence in using tools such as Circuit Mod, Cisco Packet Tracer, and AVR microcontroller kits for hands-on teaching
- 3 Integration of Theory and Practice – Educators will be able to effectively connect classroom teaching with laboratory experiments and real-world applications.
- 4 Adoption of Innovative Pedagogy – Participants will be exposed to creative teaching-learning methods, improving student engagement and concept clarity.





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**CERTIFICATE**

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**ANNASAHEB MAGAR MAHAVIDYALAYA**  
 Hadapsar, Pune – 411028 NAAC Accredited 'A+' Grade

In Association with  
 Board Of Studies (Electronics), Savitribai Phule Pune University, Pune.

**CERTIFICATE**

This is to certify that Priya Sudhir Janjalkar from ASM's College Of Commerce, Science & Information Technology, Pimpri has actively participated in ONE Day **University Level Workshop** on **"Empowering Electronics Educators: Practical Training and In-depth Syllabus Orientation for Second Year Teaching"** for **B.Sc.-CS (Electronics Science)** and **BCA(Sci.)** organized by Department of Electronics Science, Annasaheb Magar Mahavidyalaya, Hadapsar, Pune-28 and BoS – Electronics Science of Savitribai Phule Pune University, Pune On 13<sup>th</sup> of August 2025, Wednesday.

DR. V. B. MORE  
 Coordinator & Head, Dept of Electronics

DR. L. K. NIKAM  
 IQAC Coordinator

DR. N. L. GHORPADE  
 Principal, AMC, Hadapsar  
 Member, Management Council, SPPU,

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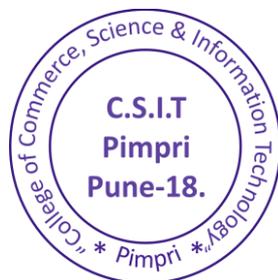
**CERTIFICATE**

This is to certify that Chaitanya Vinayak Hanote from ASM college of arts science and information technology, Pimpri has actively participated in ONE Day **University Level Workshop** on **"Empowering Electronics Educators: Practical Training and In-depth Syllabus Orientation for Second Year Teaching"** for **B.Sc.-CS (Electronics Science)** and **BCA(Sci.)** organized by Department of Electronics Science, Annasaheb Magar Mahavidyalaya, Hadapsar, Pune-28 and BoS – Electronics Science of Savitribai Phule Pune University, Pune On 13<sup>th</sup> of August 2025, Wednesday.

DR. V. B. MORE  
 Coordinator & Head, Dept of Electronics

DR. L. K. NIKAM  
 IQAC Coordinator

DR. N. L. GHORPADE  
 Principal, AMC, Hadapsar  
 Member, Management Council, SPPU,



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